

**Notice of Allowability**

Application No.

10/630,297

Examiner

Mohsin (Ben) Benghuzzi

Applicant(s)

ADKISSON, RICHARD W.

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to January 17, 2007.
2. ☒ The allowed claim(s) is/are 1-10, 12-24, and 26 (renumbered 1-24, respectively).
3. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some\* c) ☐ None of the:
- ☐ Certified copies of the priority documents have been received.
  - ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  - ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
- (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
- 1) ☐ hereto or 2) ☐ to Paper No./Mail Date \_\_\_\_\_.
- (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

**Attachment(s)**

- ☐ Notice of References Cited (PTO-892)
- ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- ☐ Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date \_\_\_\_\_
- ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material
- ☐ Notice of Informal Patent Application
- ☐ Interview Summary (PTO-413), Paper No./Mail Date \_\_\_\_\_
- ☒ Examiner's Amendment/Comment
- ☒ Examiner's Statement of Reasons for Allowance
- ☐ Other \_\_\_\_\_

## DETAILED ACTION

### EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. In the specification, Applicant has intentionally left blank the application numbers and filing dates of co-pending applications. Examiner has inserted in the blanks the corresponding application numbers and filing dates. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it **MUST** be submitted no later than the payment of the issue fee.

The Specification is to be amended as follows:

**1) Replace paragraph 0002 with:**

---This application discloses subject matter related to the subject matter disclosed in the following commonly owned co-pending patent applications: following commonly owned co-pending patent applications: (i) "Programmable Clock Synchronizer," filed July 30, 2003; Application No. 10/630,159 (Docket No. 200207722-2), in the name(s) of: Richard W. Adkisson; (ii) "Controller Arrangement for a Programmable Clock Synchronizer," filed July 30, 2003; Application No. 10/630,182 (Docket No. 200207723-1), in the name(s) of: Richard W. Adkisson; (iii) "System and Method for Synchronizing Multiple Synchronizer Controllers," filed July 30, 2003; Application No. 10/629,989 (Docket No. 200207724-1), in the name(s) of: Richard W. Adkisson; (iv) "System and Method for

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Compensating for Skew between a First Clock Signal and a Second Clock Signal," filed July 30, 2003; Application No. 10/630,317 (Docket No. 200208009-1), in the name(s) of: Richard W. Adkisson; and (v) "Phase Detector for a Programmable Clock Synchronizer," filed July 30, 2003; Application No. 10/630,298 (Docket No. 200208010-1), in the name(s) of: Richard W. Adkisson, all of which are incorporated by reference herein.---

**2) Replace paragraph 0023 with:**

---Additional details regarding the various sub-systems described hereinabove may be found in the following commonly owned co-pending patent applications: (i)

"Programmable Clock Synchronizer," filed July 30, 2003; Application No. 10/630,159 (Docket No. 200207722-2), in the name(s) of: Richard W. Adkisson; (ii) "Controller

Arrangement for a Programmable Clock Synchronizer," filed July 30, 2003; Application No. 10/630,182 (Docket No. 200207723-1), in the name(s) of: Richard W. Adkisson; (iii)

"System and Method for Synchronizing Multiple Synchronizer Controllers," filed July 30, 2003; Application No. 10/629,989 (Docket No. 200207724-1), in the name(s) of: Richard

W. Adkisson; (iv) "System and Method for Compensating for Skew between a First Clock Signal and a Second Clock Signal," filed July 30, 2003; Application No.

10/630,317 (Docket No. 200208009-1), in the name(s) of: Richard W. Adkisson; and (v)

"Phase Detector for a Programmable Clock Synchronizer," filed July 30, 2003;

Application No. 10/630,298 (Docket No. 200208010-1), in the name(s) of: Richard W.

Adkisson, all of which are incorporated by reference herein.---

**3) In paragraph 0025, replace lines 21-26 with:**

---capture control functionality of an exemplary bus-to-core synchronizer circuit maybe found in the aforementioned co-pending U.S. patent application entitled "Programmable Clock Synchronizer," filed July 30, 2003; Application No. 10/630,159 (Docket No. 200207722-2), in the name(s) of: Richard W. Adkisson.---

**4) In paragraph 0031, replace lines 14-15 with:**

---Programmable Clock Synchronizer," filed July 30, 2003; Application No. 10/630,182 (Docket No. 200207723-1), in---

**5) In paragraph 0036, replace lines 14-16 with:**

---a Programmable Clock Synchronizer," filed July 30, 2003; Application No. 10/630,182 (Docket No. 200207723-1), in the name(s) of: Richard W. Adkisson.---

**6) In paragraph 0040, replace lines 7-10 with:**

---First Clock Signal and a Second Clock Signal," filed July 30, 2003; Application No. 10/630,317 (Docket No. 200208009-1), in the name(s) of: Richard W. Adkisson. The skew state detector block 314 determines the skew\_state signal---

***Response to Applicant's Remarks***

2. The rejection of claims 1-10, 12, 14-18, and 20-24 has been withdrawn due to the amendment. Applicant has amended base claims 1, 14, and 20 to include the allowable limitation of dependent claims 11 and 25.

***Allowable Subject Matter***

3. The following is an examiner's statement of reasons for allowance: The prior art of record fails to clearly teach or suggest a system or method for maintaining a stable synchronization state in a programmable clock synchronizer for effectuating data transfer between first circuitry in a first clock domain and second circuitry in a second clock domain, wherein said first and second clock signals have a clock cycle ratio of  $N/M \geq 1$ , the system or method comprise a first, second, and third circuit portions, wherein, the third circuit portion operates in response to a load signal, a lock signal, and a zero skew point indicator to generate a synchronization signal that is to be transmitted by said third circuit portion to said first circuitry.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

***Conclusion***

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Mohsin (Ben) Benghuzzi whose telephone number is (571) 270-1075. The examiner can normally be reached Monday through Friday, 8:30-5:00 M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mohammad Ghayour can be reached on (571) 272-3021. The fax phone

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number for the organization where this application or proceeding is assigned is 571-273-8300.

5. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Mohsin (Ben) Benghuzzi

April 10, 2007

  
MOHAMMED GHAYOUR  
SUPERVISORY PATENT EXAMINER